



Integrate Circuit

Fig. 2

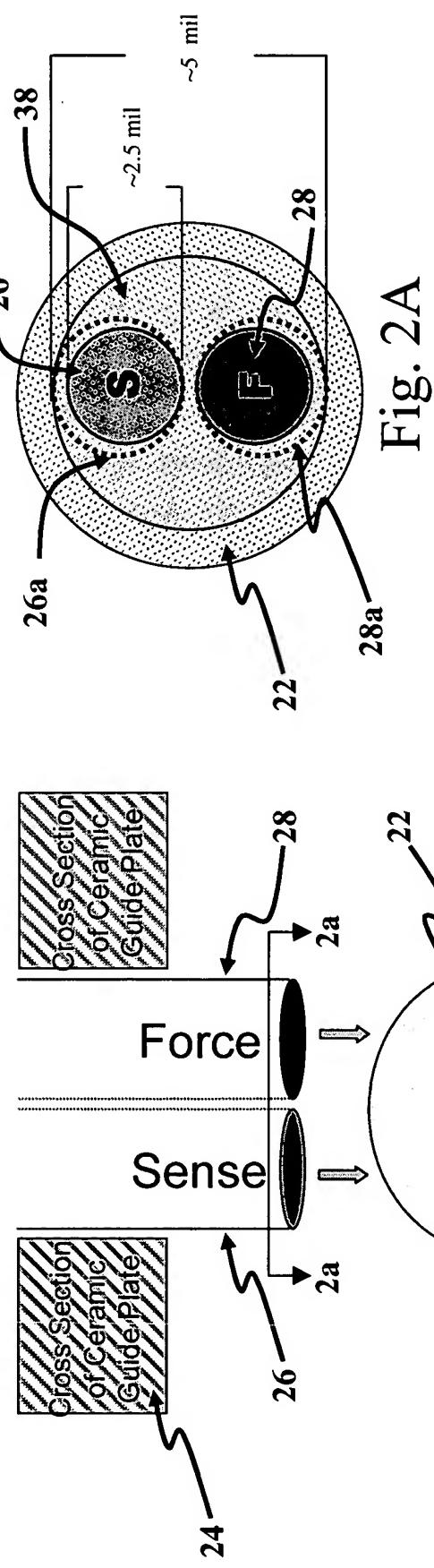


Fig. 2A

Fig. 1
Prior Art

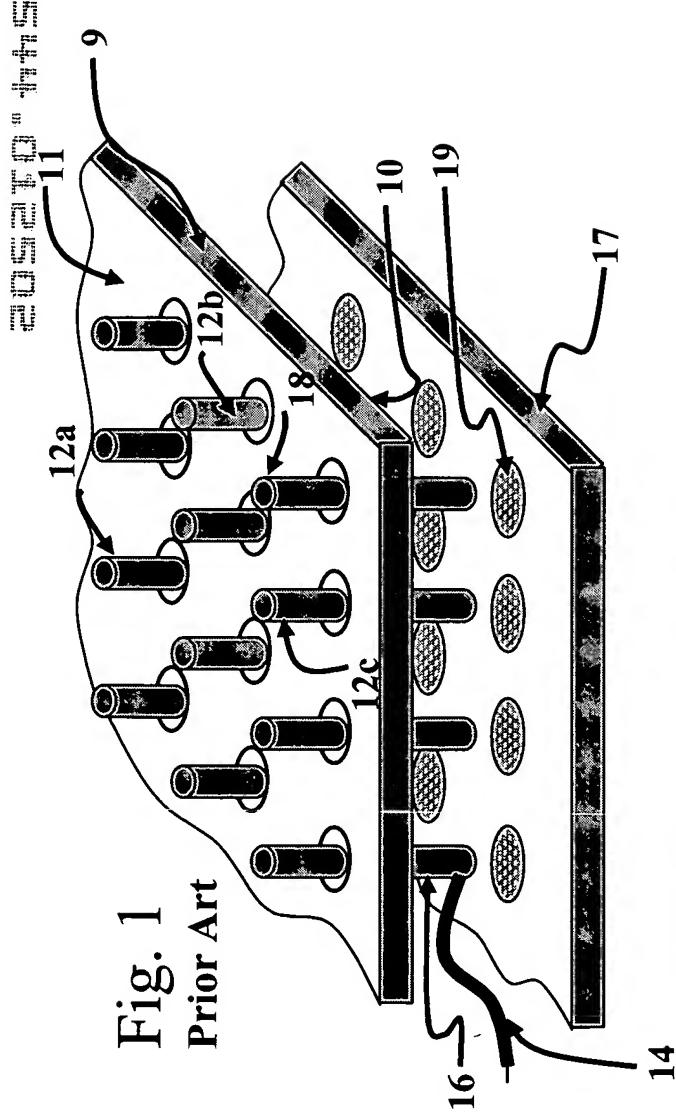


Fig. 3

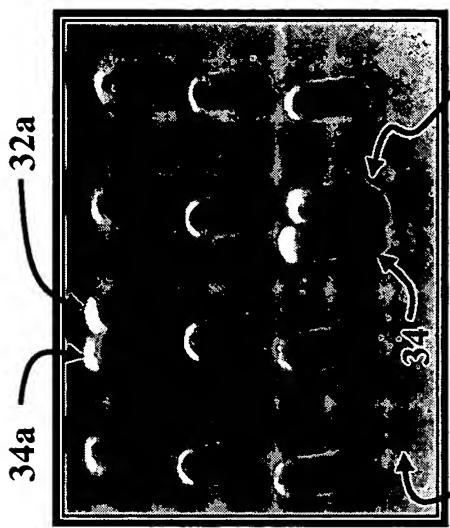


Fig. 3

